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** CONTINUING DATA *****

This application is a 371 of PCT/JP02/08284 08/15/2002

** FOREIGN APPLICATIONS *****

NONE VY

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 45	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance				
Verified and Acknowledged Examiner's Signature	<i>V. Yee</i>	Initials			

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TITLE

Semiconductor integrated circuit device and method for fabricating the same

FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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